

1

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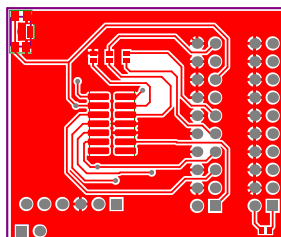
B

C

C

D

D



STMicroelectronics
Via Tolomeo 1
20010 Cornaredo (MI)
Italy

ENGINEER:

PCB DESIGNER:

DATE:
9/21/2022FILE NAME:
STEVAL-MKIGBV4.PcbDoc

TITLE:

STEVAL-MKIGBV4.PcbDoc

PART NO.:

DWG NO:
Top LayerREV:
1.0

SCALE:

1

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A

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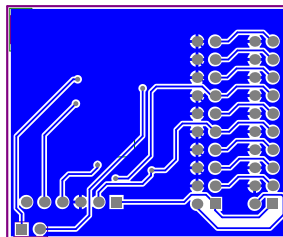
B

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STMicroelectronics
Via Tolomeo 1
20010 Cornaredo (MI)
Italy

ENGINEER:

PCB DESIGNER:

DATE:
9/21/2022FILE NAME:
STEVAL-MKIGBV4.PcbDoc

TITLE:

STEVAL-MKIGBV4.PcbDoc

PART NO.:

DWG NO:
Bottom LayerREV:
1.0

SCALE:

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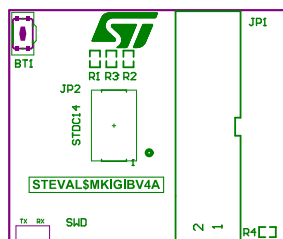
B

C

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STMicroelectronics
Via Tolomeo 1
20010 Comaredo (MI)
Italy

ENGINEER:

PCB DESIGNER:

DATE:
9/21/2022FILE NAME:
STEVAL-MKIGIBV4.PcbDoc

TITLE:

STEVAL-MKIGIBV4.PcbDoc

PART NO.:

DWG NO:
Top Silkscreen OverlayREV:
1.0

SCALE:

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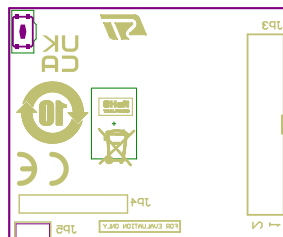
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STMicroelectronics
Via Tolomeo 1
20010 Cornaredo (MI)
Italy

ENGINEER:

PCB DESIGNER:

DATE:
9/21/2022FILE NAME:
STEVAL-MKIGBV4.PcbDoc

TITLE:

STEVAL-MKIGBV4.PcbDoc

PART NO.:

DWG NO:
Bottom Silkscreen OverlayREV:
1.0

SCALE:

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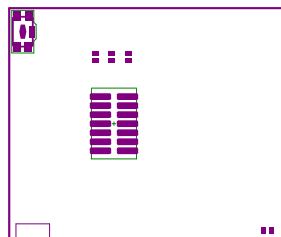
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STMicroelectronics
Via Tolomeo 1
20010 Cornaredo (MI)
Italy

ENGINEER:

PCB DESIGNER:

DATE:
9/21/2022FILE NAME:
STEVAL-MKIGBV4.PcbDoc

TITLE:

STEVAL-MKIGBV4.PcbDoc

PART NO.:

DWG NO:
Top Paste Mask PrintREV:
1.0

SCALE:

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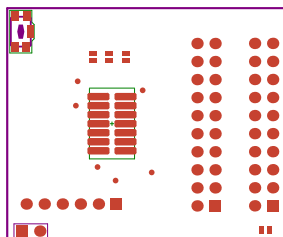
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Italy

ENGINEER:

PCB DESIGNER:

DATE:
9/21/2022FILE NAME:
STEVAL-MKIGBV4.PcbDoc

TITLE:

STEVAL-MKIGBV4.PcbDoc

PART NO.:

DWG NO:
Top Solder Mask PrintREV:
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SCALE:

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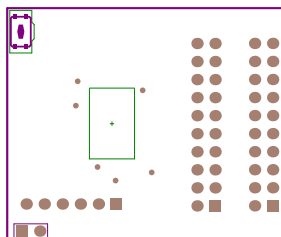
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Italy

ENGINEER:

PCB DESIGNER:

DATE:
9/21/2022FILE NAME:
STEVAL-MKIGBV4.PcbDoc

TITLE:

STEVAL-MKIGBV4.PcbDoc

PART NO.:

DWG NO:
Bottom Solder Mask PrintREV:
1.0

SCALE:

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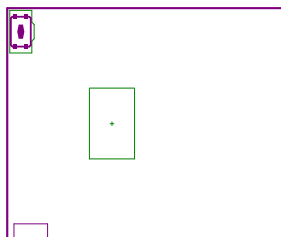
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Italy

ENGINEER:

PCB DESIGNER:

DATE:
9/21/2022FILE NAME:
STEVAL-MKIGBV4.PcbDoc

TITLE:

STEVAL-MKIGBV4.PcbDoc

PART NO.:

DWG NO:
Mechanical 1REV:
1.0

SCALE:

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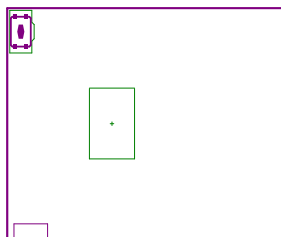
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Italy

ENGINEER:

PCB DESIGNER:

DATE:
9/21/2022FILE NAME:
STEVAL-MKIGBV4.PcbDoc

TITLE:

STEVAL-MKIGBV4.PcbDoc

PART NO.:

DWG NO:
Mechanical 13REV:
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SCALE:

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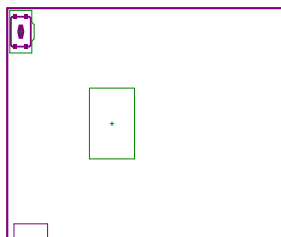
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Italy

ENGINEER:

PCB DESIGNER:

DATE:
9/21/2022FILE NAME:
STEVAL-MKIGBV4.PcbDoc

TITLE:

STEVAL-MKIGBV4.PcbDoc

PART NO.:

DWG NO:
Mechanical 15REV:
1.0

SCALE:

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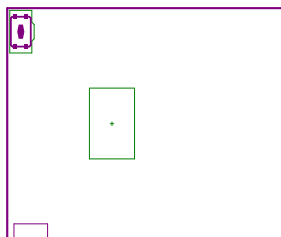
B

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Via Tolomeo 1
20010 Cornaredo (MI)
Italy

ENGINEER:

PCB DESIGNER:

DATE:
9/21/2022FILE NAME:
STEVAL-MKIGBV4.PcbDoc

TITLE:

STEVAL-MKIGBV4.PcbDoc

PART NO.:

DWG NO:
Route Tool PathREV:
1.0

SCALE:

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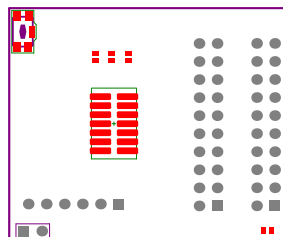
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Italy

ENGINEER:

PCB DESIGNER:

DATE:
9/21/2022FILE NAME:
STEVAL-MKIGBV4.PcbDoc

TITLE:

STEVAL-MKIGBV4.PcbDoc

PART NO.:

DWG NO:
Top Pad MasterREV:
1.0

SCALE:

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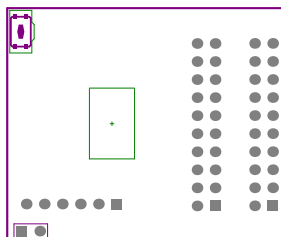
B

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20010 Cornaredo (MI)
Italy

ENGINEER:

PCB DESIGNER:

DATE:
9/21/2022FILE NAME:
STEVAL-MKIGBV4.PcbDoc

TITLE:

STEVAL-MKIGBV4.PcbDoc

PART NO.:

DWG NO:
Bottom Pad MasterREV:
1.0

SCALE:

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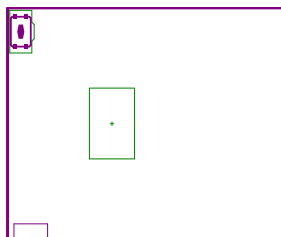
B

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Italy

ENGINEER:

PCB DESIGNER:

DATE:
9/21/2022FILE NAME:
STEVAL-MKIGBV4.PcbDoc

TITLE:

STEVAL-MKIGBV4.PcbDoc

PART NO.:

DWG NO:
Keep Out LayerREV:
1.0

SCALE:

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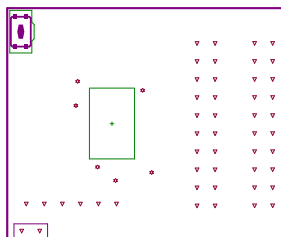
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ENGINEER:

PCB DESIGNER:

DATE:
9/21/2022FILE NAME:
STEVAL-MKIGBV4.PcbDoc

TITLE:

STEVAL-MKIGBV4.PcbDoc

PART NO.:

DWG NO:
Drill Drawing For (Top Layer - Bottom Layer)REV:
1.0

SCALE:

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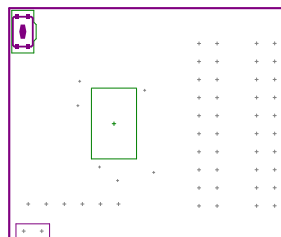
B

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ENGINEER:

PCB DESIGNER:

DATE:
9/21/2022FILE NAME:
STEVAL-MKIGBV4.PcbDoc

TITLE:

STEVAL-MKIGBV4.PcbDoc

PART NO.:

DWG NO:
Drill Guide For (Top Layer - Bottom Layer)REV:
1.0

SCALE:

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